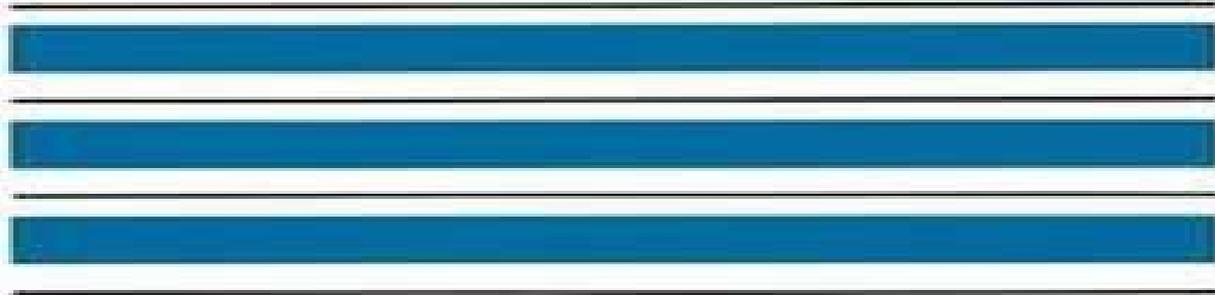


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# **FET Modeling for Circuit Simulation**

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**Judd E. Hollander**



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